

CLAIMS

1. (Currently Amended) A method for storing macroblocks in a memory, said method comprising:

decoding a macroblock, thereby resulting in a decoded macroblock, said decoded macroblock comprising pixels; and

executing an instruction, wherein the instruction causes:

writing the decoded macroblock to the memory, wherein writing the macroblock to the memory further comprises:

writing a matrix of luminance pixels to a first portion of the memory;

writing a first matrix of chrominance pixels to a second portion of the memory;

writing a second matrix of chrominance pixels to a third portion of the memory; and

the first portion, second portion, and third portion being contiguous.

2. (Cancelled)

3. (Currently Amended) A method for storing macroblocks in a memory, said method comprising:

decoding five macroblocks, thereby resulting in decoded macroblocks, said decoded macroblocks comprising pixels; and

executing an instruction, wherein the instruction causes:

writing the five macroblocks to the memory, wherein writing the macroblock to the memory further comprises:

writing five matrices of luminance pixels to a first portion of the memory;

writing a first five matrices of chrominance pixels to a second portion of the memory;

writing a second five matrices of chrominance pixels to a third portion of the memory; and

the first portion, second portion, and third portion being contiguous.

4. (Cancelled).

5. (Currently Amended) A circuit for storing macroblocks, said circuit comprising:

a decoder for decoding macroblocks; and

a computer readable medium storing an executable instruction, wherein the instruction causes:

writing the macroblock to the memory, wherein writing the macroblock to the memory further comprises:

writing a matrix of luminance pixels to a first portion of the memory;

writing a first matrix of chrominance pixels to second portion of the memory;

writing a second matrix of chrominance pixels to a third portion of the memory; and

the first portion, second portion, and third portion being contiguous.

6. (Cancelled).

7. (Currently Amended) A circuit for storing macroblocks, said circuit comprising:

a decoder for decoding five macroblocks, thereby resulting in decoded macroblocks, said decoded macroblocks comprising pixels; and

a computer readable medium storing an executable instruction, wherein the instruction causes:

writing the five macroblocks to the memory, wherein writing the macroblock to the memory further comprises:

writing five matrices of luminance pixels to a first portion of the memory;

writing a first five matrices of chrominance pixels to a second portion of the memory;

writing a second five matrices of chrominance pixels to a third portion of the memory; and

the first portion, second portion, and third portion being contiguous.

8. (Cancelled).

Please add the following claims:

9. (New) The method of claim 1, wherein the second portion and the third portion form portions of a plurality of data words.